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CONFIRMATION NO. 1313

Bib Data Sheet

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|-----------------------------|-----------------------------------|--------------|------------------------|------------------------------------|
| SERIAL NUMBER 10/729,457 | FILING DATE 12/05/2003 RULE | CLASS 327 | GROUP ART UNIT 2816 | ATTORNEY DOCKET NO. NECE 20.780 |
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APPLICANTS

Minari Arai, Tokyo, JAPAN;

** CONTINUING DATA *None*** FOREIGN APPLICATIONS *Yes, LAR*

JAPAN 2002-354993 12/06/2002

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/09/2004

| Foreign Priority claimed 35 USC 119 (a-d) conditions met | <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Initials | STATE OR COUNTRY JAPAN | SHEETS DRAWING 11 | TOTAL CLAIMS 4 | INDEPENDENT CLAIMS 4 |
|---|---|------------------------------|-------------------------|----------------------|----------------------------|
| Verified and Acknowledged Examiner's Signature | | | | | |

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TITLE

Semiconductor integrated circuit device and method of detecting delay error in the same

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| FILING FEE RECEIVED 856 | FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following: | <input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other <input type="checkbox"/> Credit |
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